

FIG. 1

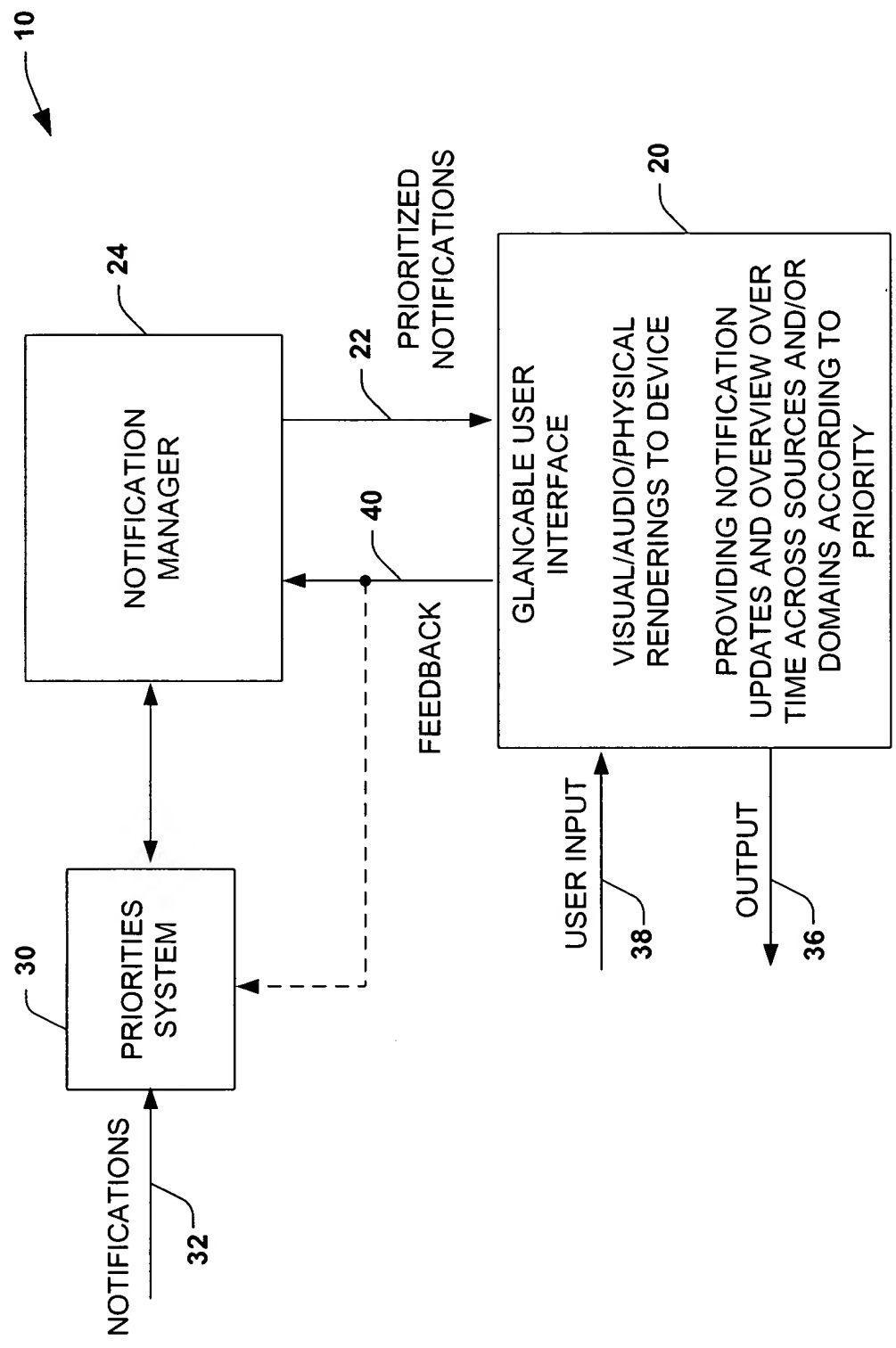


Fig. 1

FIG. 2

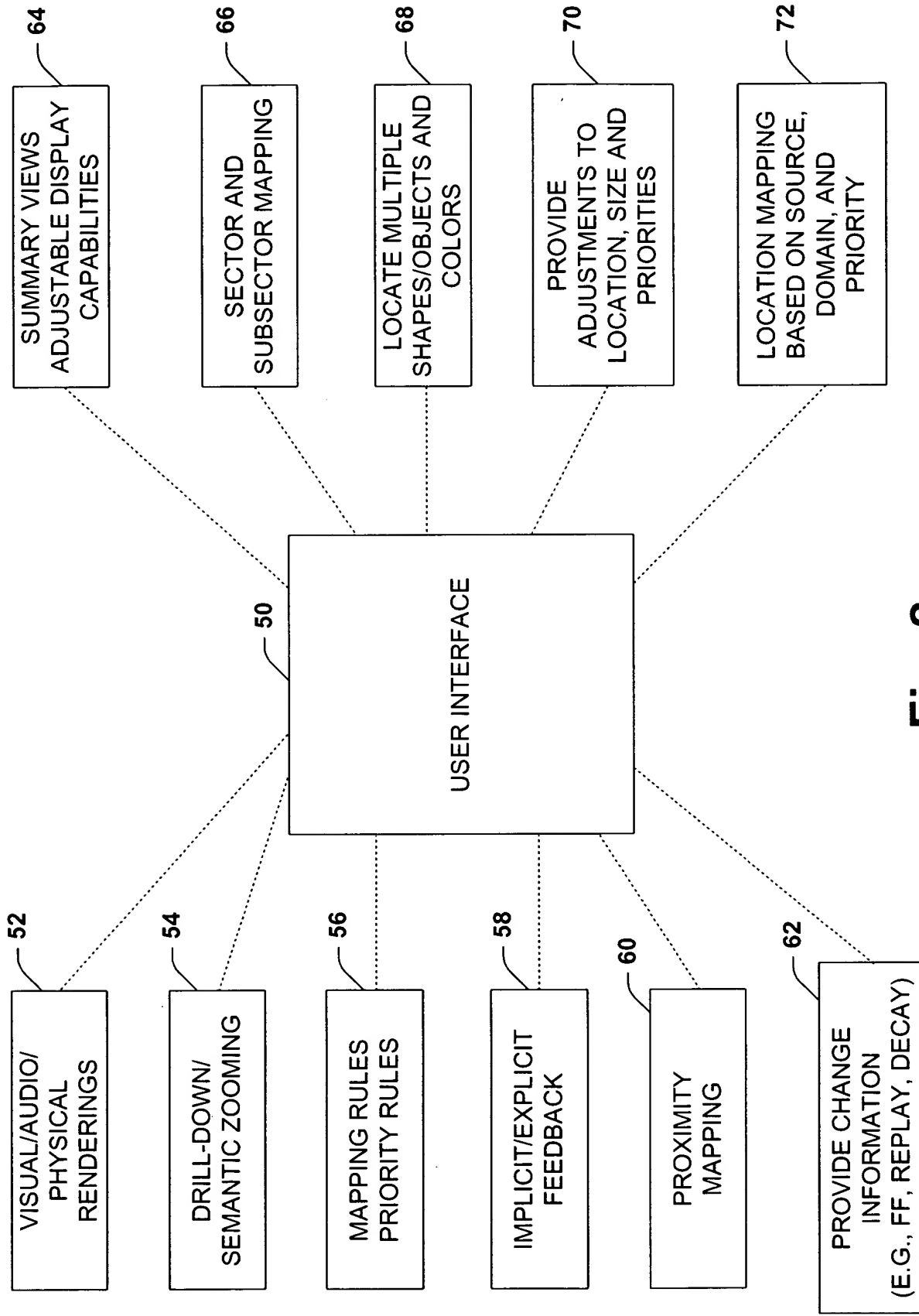
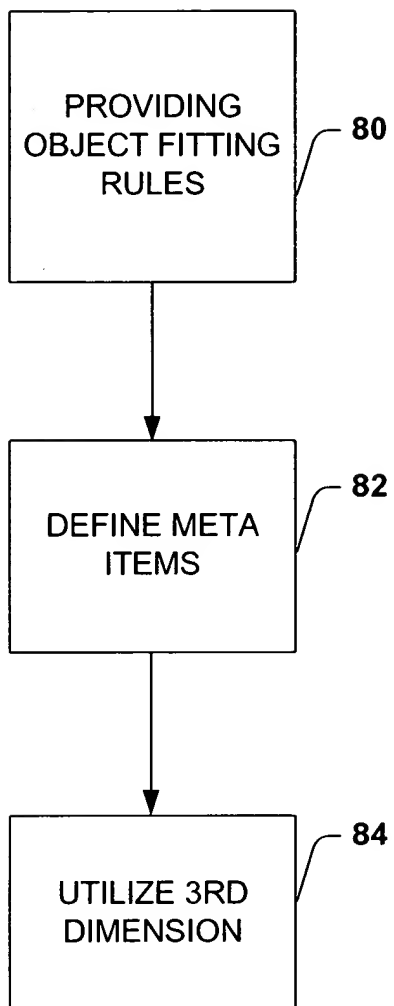


Fig. 2

FIG. 3



**Fig. 3**

**Fig. 4**

FIG. 5

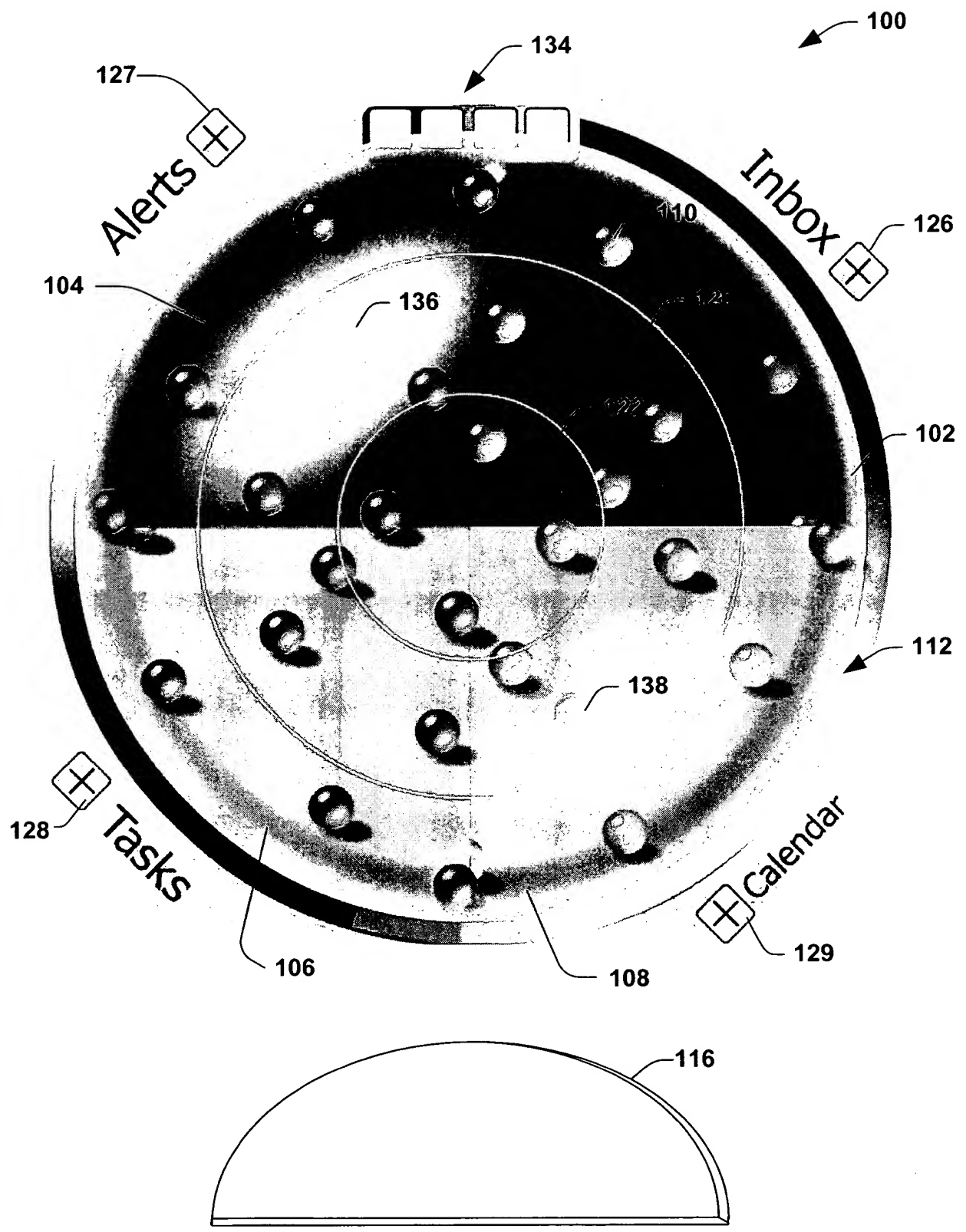


Fig. 5

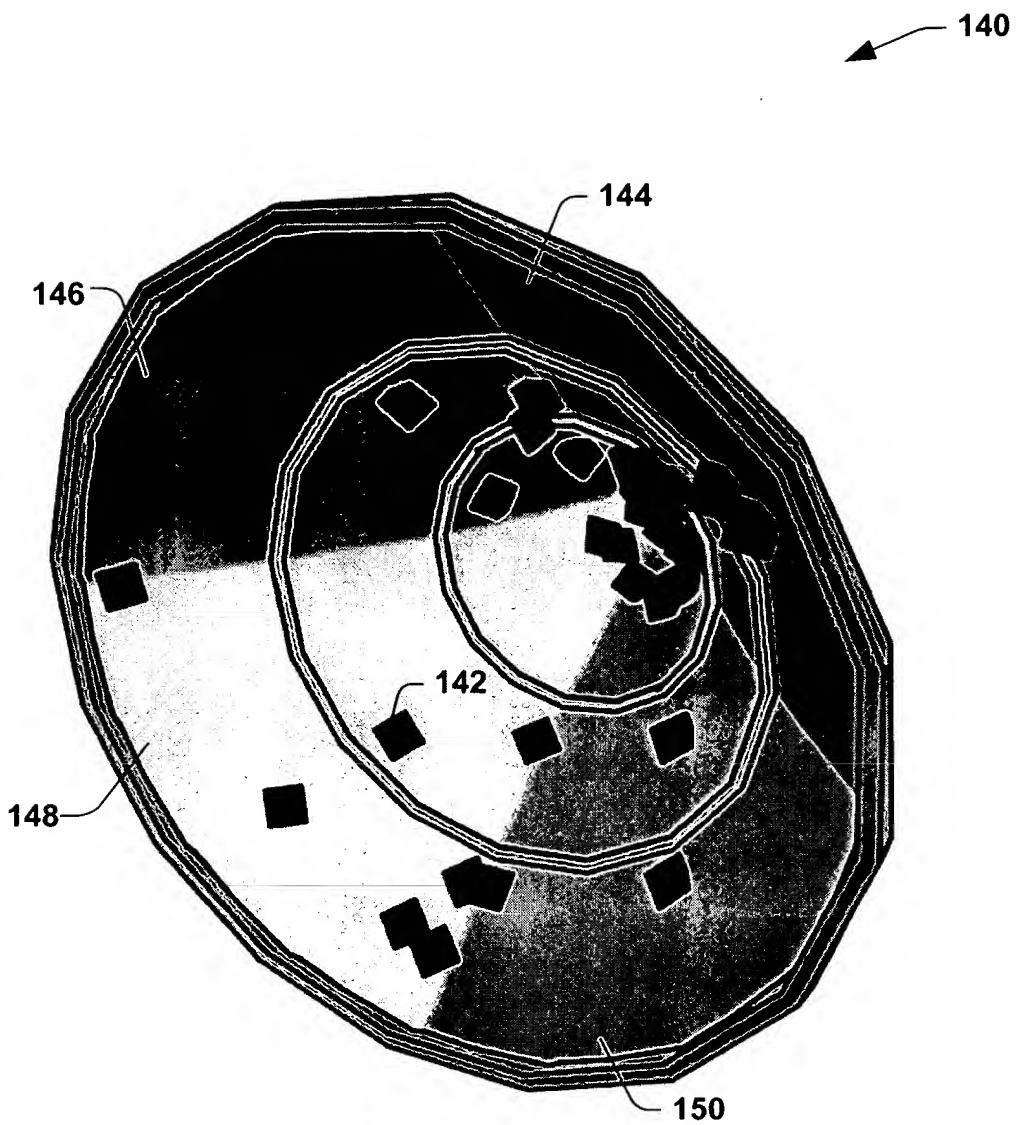
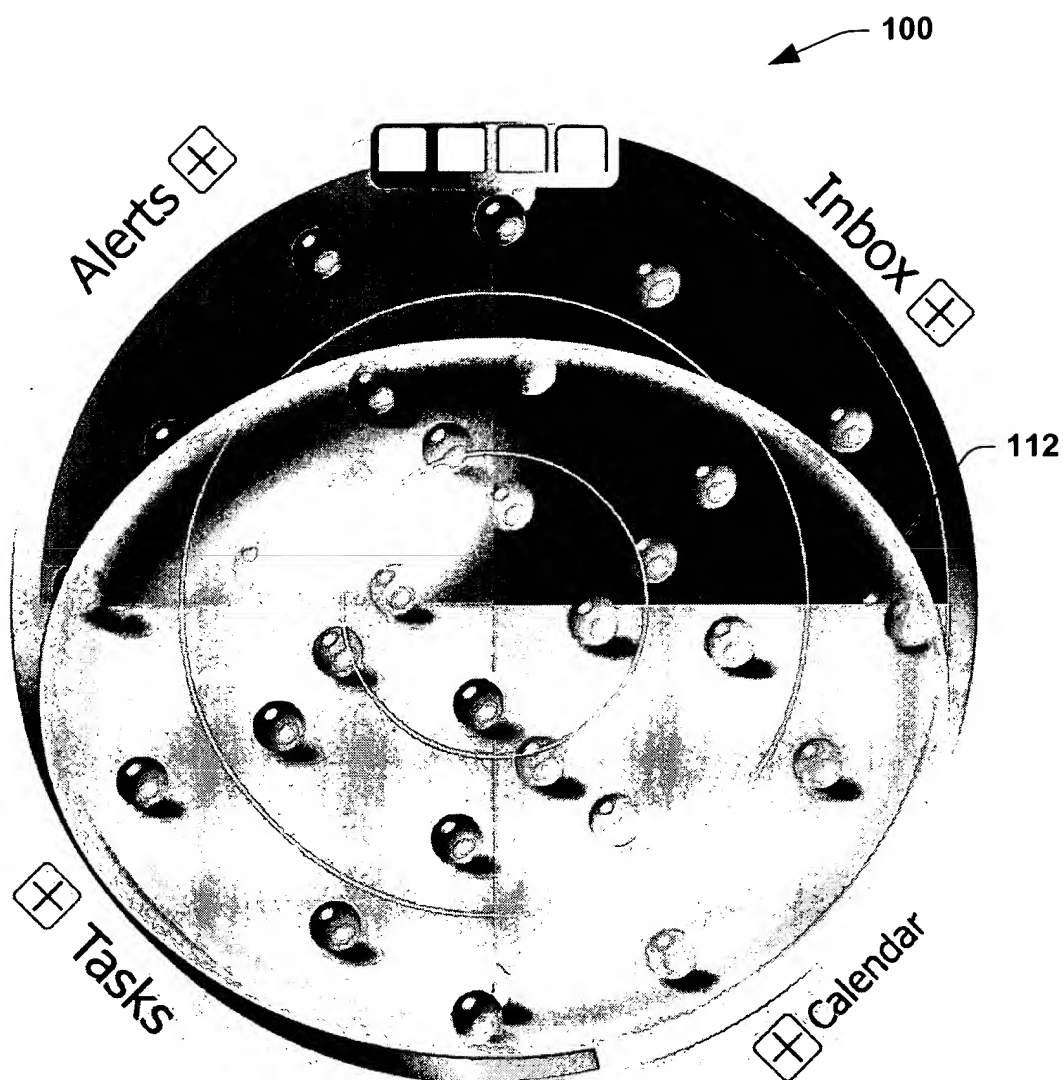


Fig. 6





**Fig. 8**



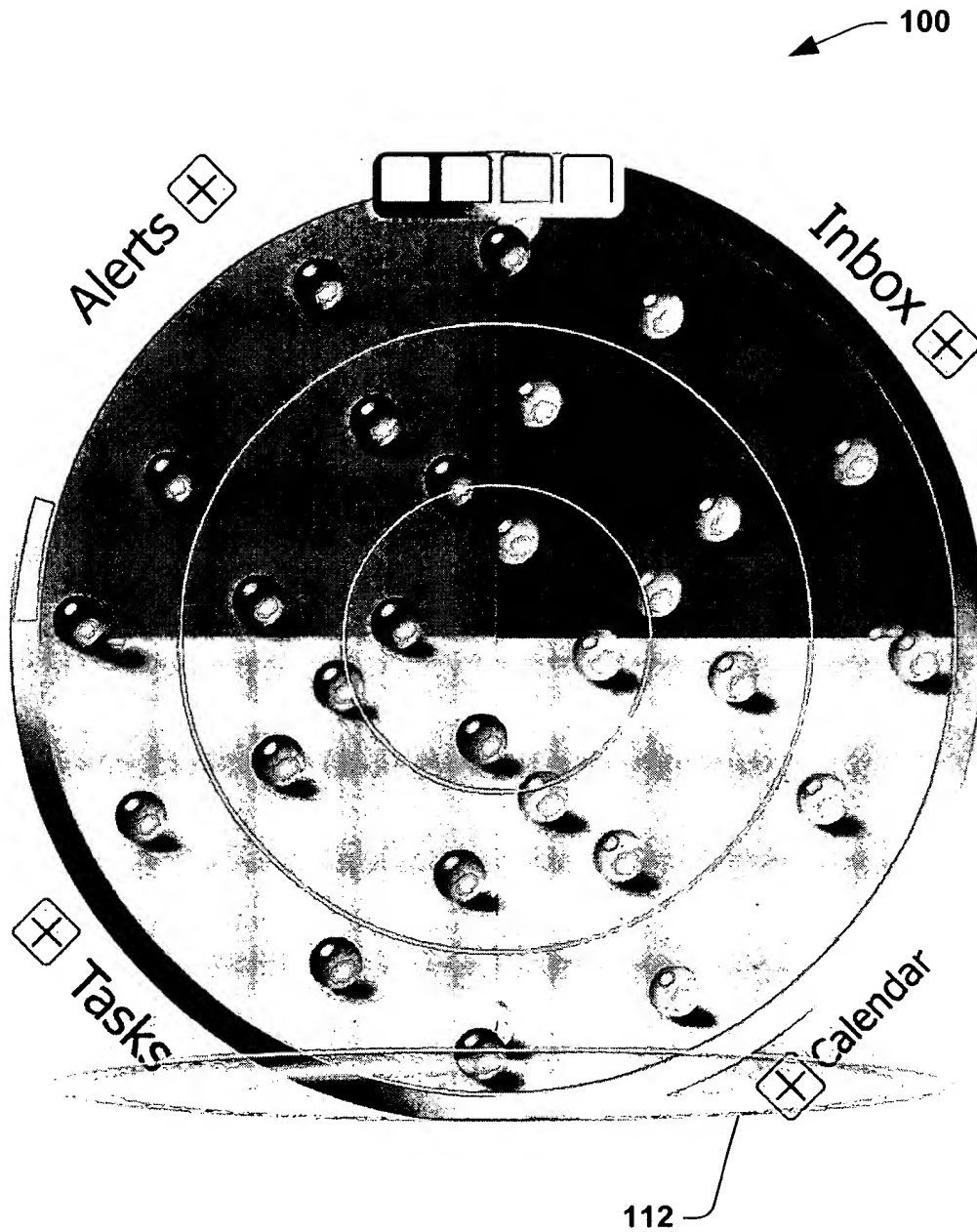


Fig. 9

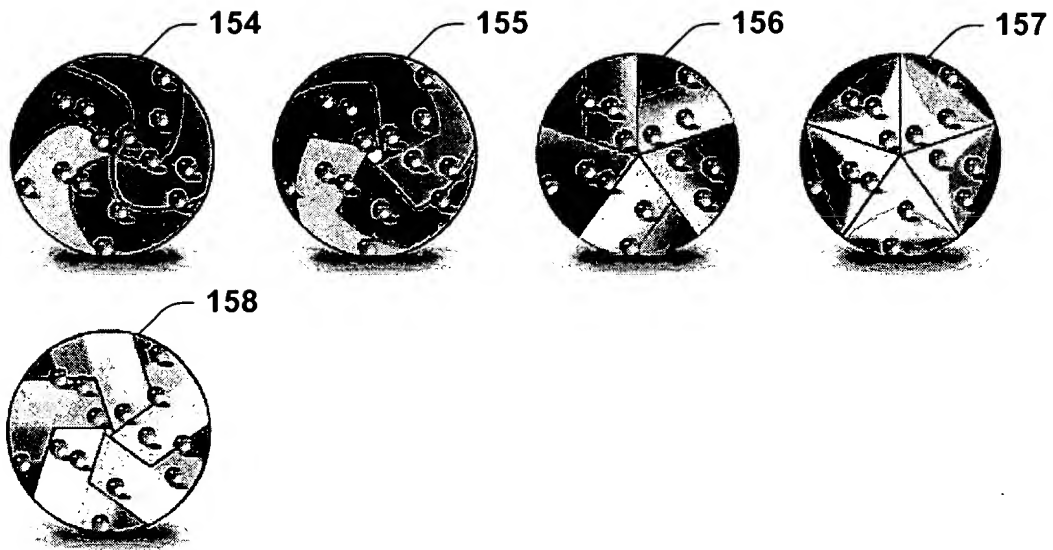


Fig. 10

000000-250510

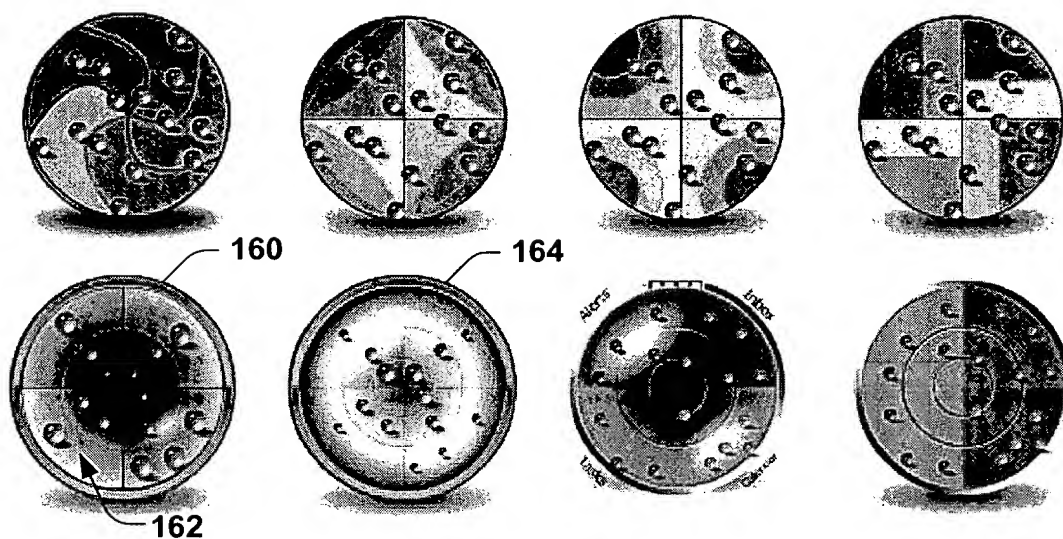


Fig. 11

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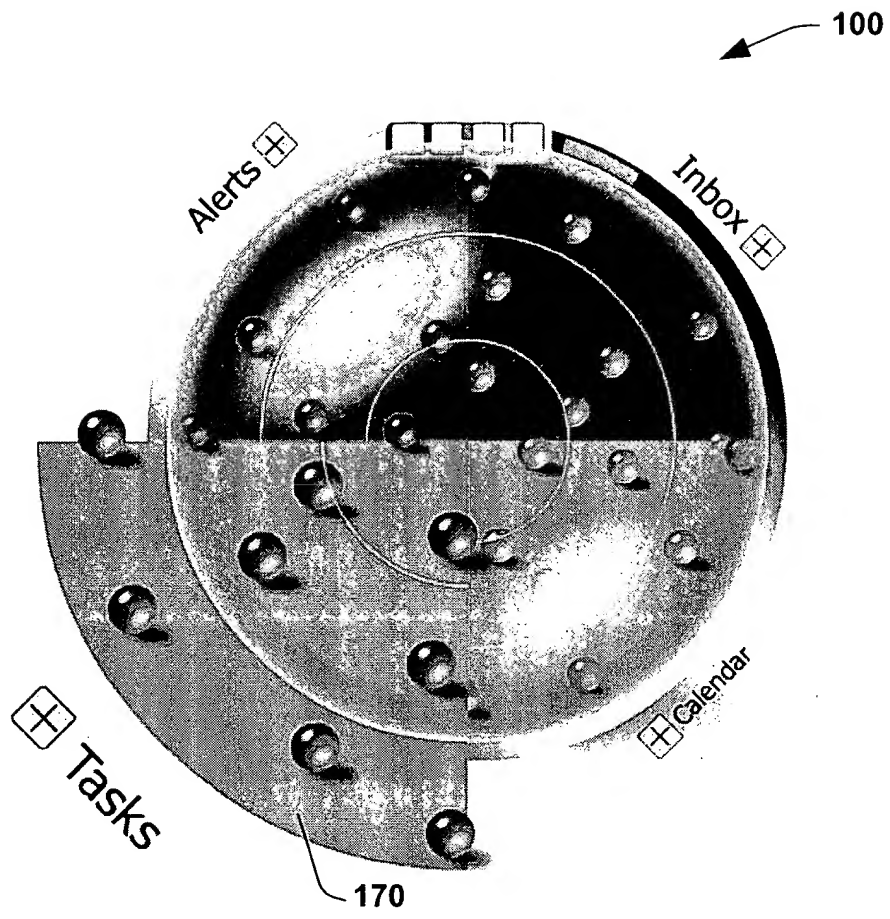


Fig. 12

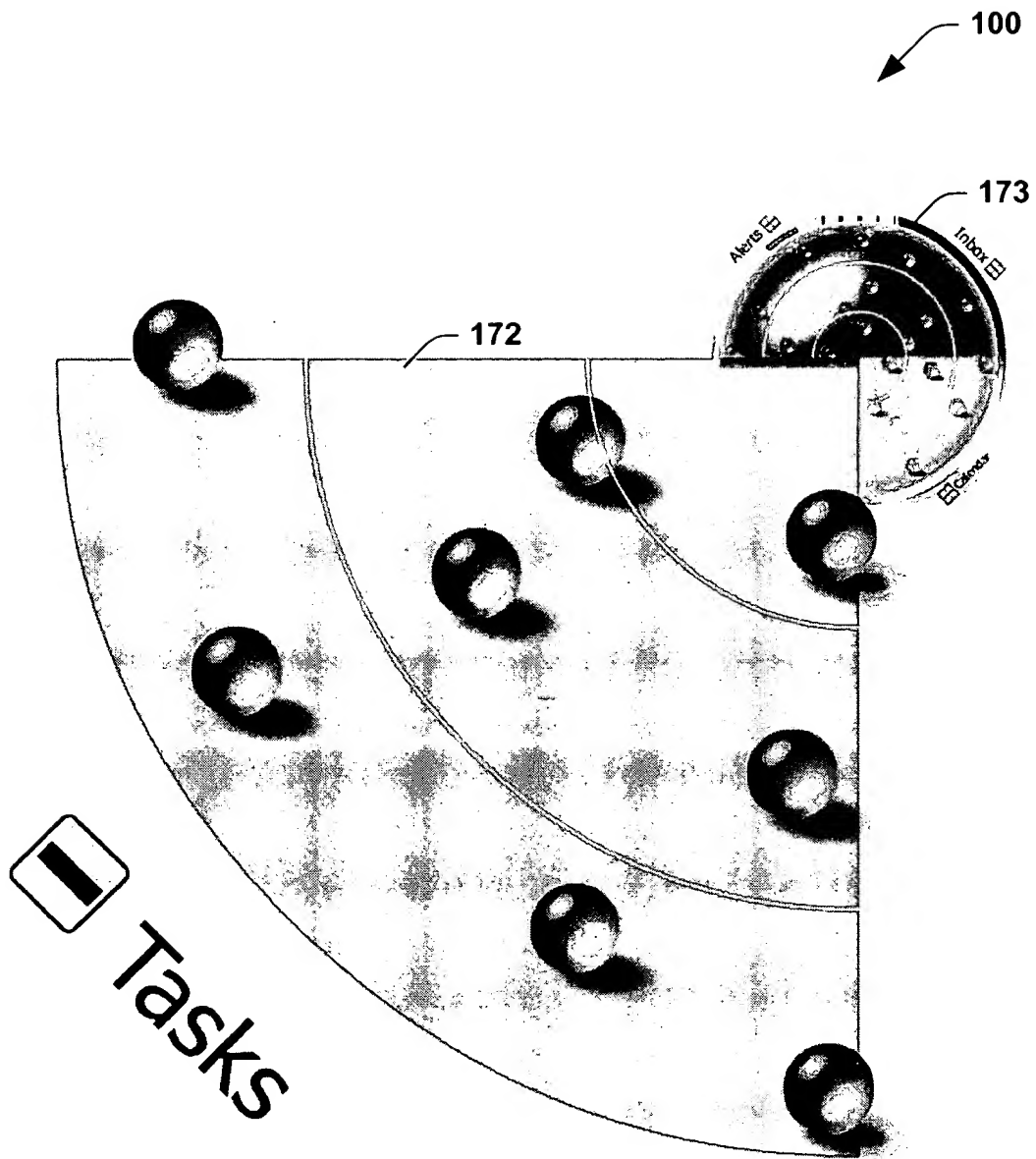


Fig. 13





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105190-2568880

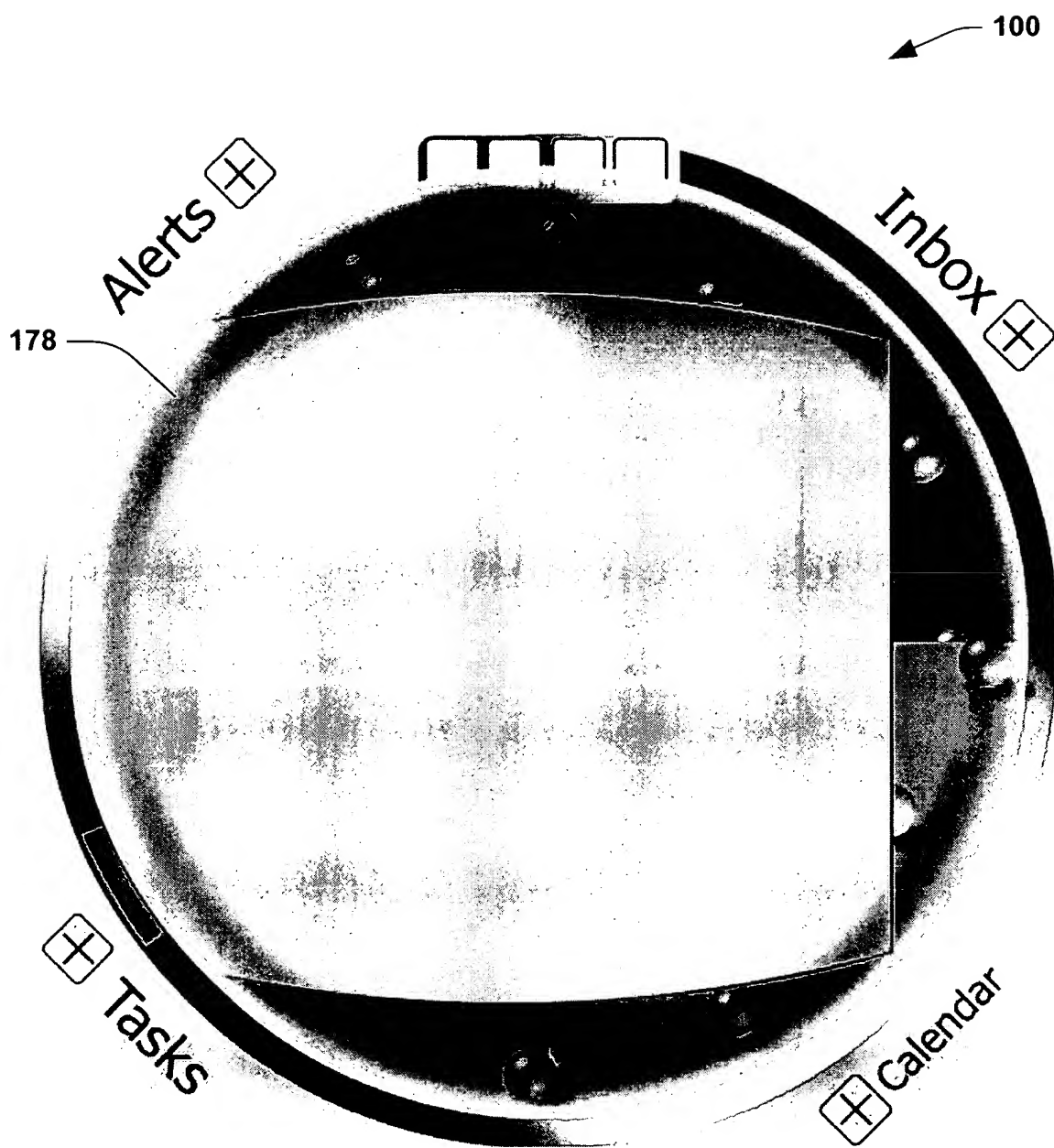
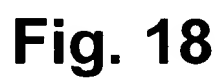
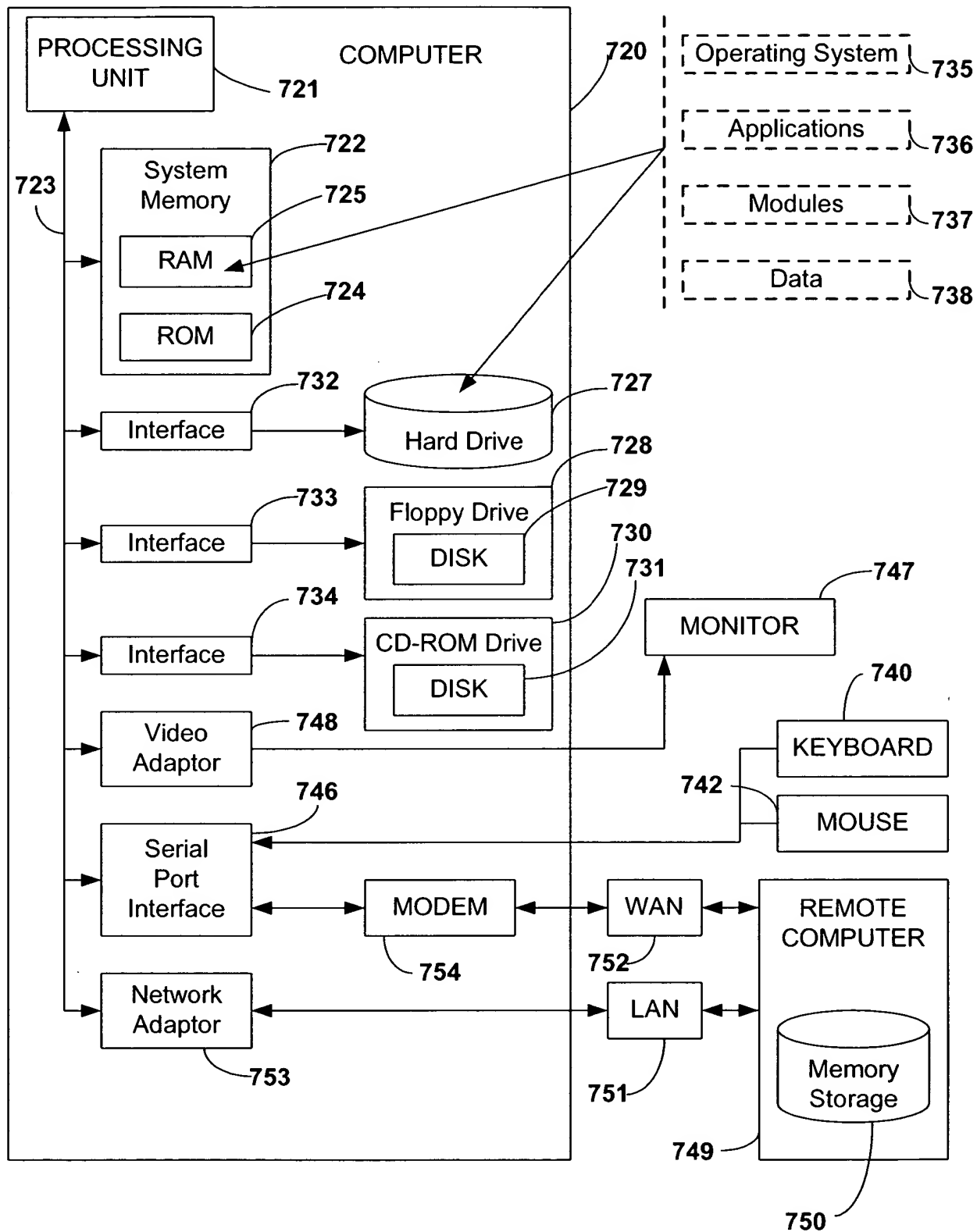


Fig. 16









**Fig. 19**

The diagram shows a large rectangular box labeled 800 at the bottom left. Inside this box, there are seven smaller rectangular boxes arranged in two columns. The left column contains three boxes: 802 PROCESSOR(S), 804 MEMORY, and 806 STORAGE. The right column contains three boxes: 810 INPUT DEVICE(S), 812 DISPLAY, and 814 OUTPUT DEVICE(S). At the bottom left of the main box is a box labeled 808 COMMUNICATIONS COMPONENT. The label 800 is connected to the bottom left corner of the main box by a wavy line.

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graph TD; 800[800] --- 802[802 PROCESSOR(S)]; 800 --- 810[810 INPUT DEVICE(S)]; 800 --- 804[804 MEMORY]; 800 --- 812[812 DISPLAY]; 800 --- 806[806 STORAGE]; 800 --- 814[814 OUTPUT DEVICE(S)]; 800 --- 808[808 COMMUNICATIONS COMPONENT];
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**Fig. 20**